

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	2	((discrete adj time) and (storage) and display).clm.	US-PGPUB	OR	OFF	2005/11/30 15:22
L5	35	(discrete and register and display).clm.	US-PGPUB	OR	OFF	2005/11/30 15:14
L6	1	(schematic and display and (fan adj (in out))).clm.	US-PGPUB	OR	OFF	2005/11/30 15:14
L8	31	((display view screen) and (fanin fanout (fan adj (in out)))).clm.	US-PGPUB	OR	OFF	2005/11/30 15:17
L9	60	((register flip latch) and (fanin fanout (fan adj (in out)))).clm.	US-PGPUB	OR	OFF	2005/11/30 15:18
L10	50	9 not 8	US-PGPUB	OR	OFF	2005/11/30 15:18
L17	1	(clocked and logic and (display screen view) and (fanin fanout (fan adj (in out) ((input output) adj function)))).clm.	US-PGPUB	OR	OFF	2005/11/30 15:24
L18	6	(clocked and logic and (fanin fanout (fan adj (in out) ((input output) adj function)))).clm.	US-PGPUB	OR	OFF	2005/11/30 15:24

*Interference Search*  
*Jef*

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	374	703/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:18
S2	207	(fan adj (in out)) same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 11:29
S3	108	(fan adj (in out)) with display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 11:29
S5	2	S2 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 12:32
S6	4	S2 and "716".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 11:31
S7	8	S2 and "715".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 11:32
S8	32	S2 and "345".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 11:32
S9	46	S5 or S6 or S7 or S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 12:33
S10	83	S3 not S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:00

S11	740	((fan adj (in out)) fanin fanout) same view	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 13:01
S12	714	S11 not S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 12:39
S14	7	S12 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 12:41
S15	41	S12 and "716".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 13:01
S17	10	S12 and "345".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 13:04
S18	0	S12 and "715".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 13:04
S20	72	(fanin (fan adj (in out))) adj cone	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 17:51
S21	12	S20 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 17:53
S22	0	S20 and "345".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 17:54
S23	49	S20 and "716".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:00

S24	0	S20 and "715".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:00
S25	149	(logic adj cone)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:00
S26	26	S25 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:05
S27	74	S25 and "716".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:05
S28	61	S27 not S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:18
S29	0	S25 and "345".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:18
S30	0	S25 and "715".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:32
S31	682	(input adj function) same (register (flip adj flop) latch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:32
S32	6	S31 and "703".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:35
S33	7	S31 and "706".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:36

S34	19	S31 and "345".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:37
S35	5	S31 and "715".clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/29 18:37
S36	913	716/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:19
S38	684	716/5.ccls. and @ad<="20010401"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:20
S39	86	S38 and (fanin fanout (fan adj (in out)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:27
S40	13	S38 and ((register storage memory flip latch) same debug\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:30
S41	150	S38 and ((register storage memory flip latch) with (display view screen))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:30
S42	120	S41 not S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:31
S43	114	S42 not S40	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:54
S44	3	"6718520".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 11:54

S54	473	S45 not S47 not S49 not S52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:16
S55	253	715/771.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:17
S56	69	715/771.ccls. and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:33
S57	57	715/771.ccls. and simulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:34
S58	36	S57 not S56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:36
S59	879	703/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:36
S60	564	703/14.ccls. and @ad<="20010401"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:53
S61	1000	345/440.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:53
S62	694	345/440.ccls. and @ad<="20010401"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:54
S63	412	S62 and ((register storage memory flip latch) with (display view screen))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:54

S64	135	S62 and ((register storage memory flip latch) and (simulat\$5 emulat\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 13:55
-----	-----	---	---	----	-----	------------------